



Sergio
Sapienza

DATE OF BIRTH:
[REDACTED]

CONTACT

Nationality: Italian

Gender: Male

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

WORK EXPERIENCE

04/01/2021 - CURRENT - Bologna, Italy

Research fellow

National Research Council of Bologna - Institute for Microelectronics and Microsystems

Develop of Micro Electro-Mechanical Systems (MEMS)

<https://www.bo.imm.cnr.it/unit/content/home-page>

22/12/2017 - 31/12/2020 - Bologna, Italy

Associate with collaboration assignment

National Research Council of Bologna - Institute for Microelectronics and Microsystems

- Electronic Measurements
- Data management and analysis

<https://www.bo.imm.cnr.it/unit/content/home-page> / Bologna, Italy

01/01/2018 - 31/10/2018 - Parma, Italy

Tutor on Electronics Engineer

Università degli Studi di Parma

- Semiconductor devices
- Electronic Instrumentation and Sensors
- Power Circuits and Systems
- Electronics for Renewable Energy

Parma, Italy

07/2017 - 05/2018 - Parma, Italy

Participant of a cooperative startup creation

Participation on the call for cooperative startups organized by Coopstartup Emilia Ovest

Project focus: Blockchain and cryptocurrencies

EDUCATION AND TRAINING

01/11/2017 - 31/12/2020 - Parma, Italy

PhD on Information Technologies

University of Parma

- Electronic Measurements
- Physical simulations of electronic devices
- Data management and analysis

Field(s) of study

- Engineering, manufacturing and construction
- Electronic Engineering

EQF level 8

10/2014 - 10/2017 - Parma, Italy

Master's Degree in Electronic Engineering

University of Parma

Measurement and simulation of lifetimes of carries in PIN diodes in SiC by OCVD technique | EQF level 7

10/2010 – 10/2014 – Parma, Italy

Bachelor of Electronic and communications engineering

University of Parma

EQF level 6

2004 – 2009 – Catania, Italy

Scientific High School Diploma

State Scientific High School G. Galilei

2007 – Catania, Italy

English Diploma Level B1

British Institutes

LANGUAGE SKILLS

MOTHER TONGUE(S): Italian

OTHER LANGUAGE(S):

Inglese

Listening
B2

Reading
B2

**Spoken
production**
B2

**Spoken
interaction**
B2

Writing
B2

PUBLICATIONS

Correlation between OCVD carrier lifetime vs temperature measurements and reverse recovery behavior of the body diode of SiC power MOSFETs

2020

Sapienza S., Sozzi G., Santoro D., Cova P., Delmonte N., Verrini G., Chiorboli G., Correlation between OCVD carrier lifetime vs temperature measurements and reverse recovery behavior of the body diode of SiC power MOSFETs, *Microelectronics Reliability.*, vol. 113, p. 113937, 2020.

Smart soling sensor for PV modules

2020

Simonazzi M., Chiorboli, G., Cova P., Menozzi R., Santoro D., Sapienza S., Sciancalepore C., Sozzi G., Delmonte N., Smart soling sensor for PV modules, *Microelectronics Reliability.*, vol. 114, p. 113789, 2020.

Activation Energy for the Post Implantation Annealing of 1019 cm⁻³ and 1020 cm⁻³ Ion Implanted Al in 4H SiC

2019

Nipoti R., Canino M. C., Sapienza S., Bellettato M., Sozzi G., Alfieri G., Activation Energy for the Post Implantation Annealing of 1019 cm⁻³ and 1020 cm⁻³ Ion Implanted Al in 4H SiC, *Materials Science Forum*, vol. 963, pp. 416–419, 2019.

CONFERENCES AND SEMINARS

Conferences

- Participation on 12th European Conference on Silicon Carbide and Related Materials and presentation of a poster entitled: "On the effect of experimental set-up on OCVD carrier lifetime measurements in 4H-SiC vertical p+-i-n diode", Sozzi G. , Sapienza S. , Chiorboli G. , Nipoti R. , 12th ECSCRM 2018, Birmingham UK, September 2018. https://warwick.ac.uk/fac/sci/eng/ecscrm2018/programme/ecscrm_programme_2018.pdf
- D. Santoro, N. Rocchi, S. Sapienza, et al., "Development of a PV modules soiling monitoring system for smart maintenance", 34th DCIS 2019, Bilbao (Spain), November 2019, presented as a poster. <https://dcis2019.org/wp-content/uploads/2019/11/DCIS2019.pdf>

Seminars

BluPeak Summer School : change programs & change readiness

ORGANISATIONAL SKILLS

Organisational skills

Good organisational skills acquired during the university years

COMMUNICATION AND INTERPERSONAL SKILLS

Communication and interpersonal skills

Good communication skills obtained during the university years

PROFESSIONAL SKILLS

Professional skills

- Excellent mastery of electronic measurement tools
- Excellent mastery of software for numerical calculation (Matlab)
- Excellent mastery of software for data analysis (Origin)
- Excellent mastery of Microsoft Office tools (Word, Excel, PowerPoint)
- Excellent mastery of software for electronic design (LTspice, Advanced Design System)
- Excellent mastery of software for the physical simulation of devices (Synopsys Sentaurus Device)
- Good mastery of software for multiphysics simulation of devices (Comsol Multiphysics)
- Good mastery of hardware description languages (VHDL, Verilog)
- Good mastery of programming languages (C, C ++, Visual basic)
- Good mastery of operating systems (Windows, CentOS, Linux)
- Good mastery of PCB design software (Altium Designer)

TREATMENT OF PERSONAL DATA

Treatment of personal data

I authorize the processing of personal data contained in my curriculum vitae based on art. 13 of Legislative Decree 196/2003 and art. 13 of EU Regulation 2016/679